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APPLICATION NO.	FIL	ING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/726,927	10/726,927 12/03/2003		Matthew L. Heston	CIS0107D1US	4687
33031	7590	11/25/2005		EXAM	INER
		ENSON ASCOLE	TRINH, MINH N		
4807 SPICE BLDG. 4, SI		KINGS KD.		ART UNIT	PAPER NUMBER
AUSTIN, T			•	3729	

DATE MAILED: 11/25/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

		Application No. Applicant(s)					
		10/726,927	HESTON ET AL.				
	Office Action Summary	Examiner	Art Unit				
		Minh Trinh	3729				
Period fo	The MAILING DATE of this communication app or Reply	ears on the cover sheet with the c	orrespondence address				
A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 1 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication. - If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication. - Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).							
Status							
1)	Responsive to communication(s) filed on <u>03 De</u>	ecember 2003					
2a)□	<u> </u>	action is non-final.					
3)	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is						
/	closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.						
Dispositi	on of Claims						
	✓ Claim(s) <u>13-62</u> is/are pending in the application.✓ 4a) Of the above claim(s) is/are withdrawn from consideration.						
6)□							
7)	Claim(s) is/are objected to.						
′_	Claim(s) <u>13-62</u> are subject to restriction and/or	election requirement					
·		cicolion requirement.					
Applicati	on Papers						
9)□	The specification is objected to by the Examine	r.					
10)☐ The drawing(s) filed on is/are: a)☐ accepted or b)☐ objected to by the Examiner.							
	Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).						
	Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).						
11)	11)☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.						
Priority ι	ınder 35 U.S.C. § 119						
 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a) All b) Some * c) None of: 1. Certified copies of the priority documents have been received. 2. Certified copies of the priority documents have been received in Application No 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)). * See the attached detailed Office action for a list of the certified copies not received. 							
2) 🔲 Notic 3) 🔲 Inforr	e of References Cited (PTO-892) e of Draftsperson's Patent Drawing Review (PTO-948) mation Disclosure Statement(s) (PTO-1449 or PTO/SB/08) r No(s)/Mail Date	4) Interview Summary Paper No(s)/Mail Da 5) Notice of Informal P 6) Other:					

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DETAILED ACTION

Election/Restrictions

- 1. Restriction to one of the following inventions is required under 35 U.S.C. 121:
 - Claims 13-22, drawn to a circuit board assembly, classified in class 174, subclass 266.
 - Claims 28-48, drawn to a system for PCB, classified in class 361, subclass 803.
 - III. Claims 49-56, drawn to Method for interconnection PCB, classified in class 29, subclass 830.
 - IV. Claims 58-62, drawn to method of transmitting signal, classified in class 324, subclass 754.

The inventions are distinct, each from the other because of the following reasons: Inventions I and II-IV are related as subcombinations disclosed as usable together in a single combination. The subcombinations are distinct from each other if they are shown to be separately usable. In the instant case, invention I has separate utility such as circuit board assembly by disposing with zero interconnection height relative to the mother board, etc., (see claim 13, lines 4-5+). See MPEP § 806.05(d).

Inventions II and III or IV are related as process of making and product made.

The inventions are distinct if either or both of the following can be shown: (1) that the process as claimed can be used to make other and materially different product or (2) that the product as claimed can be made by another and materially different process (MPEP § 806.05(f)). In the instant case, that the process invention III and IV as claimed

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can be used to make other and materially different product such as different electronic assembly where the connection between circuit boards can be made by fusion bonding, etc.

Inventions III and IV are related as subcombinations disclosed as usable together in a single combination. The subcombinations are distinct from each other if they are shown to be separately usable. In the instant case, invention III-IV each has separate utility such as by transmitting a second signal between first circuit board and a second circuit board as required by IV which is not required by III and vice versa, etc. See MPEP § 806.05(d).

Because these inventions are distinct for the reasons given above and have acquired a separate status in the art as shown by their different classification, and the search required for Group I is not required for either of Group II-IV, restriction for examination purposes as indicated is proper.

Because these inventions are distinct for the reasons given above and have acquired a separate status in the art because of their recognized divergent subject matter, restriction for examination purposes as indicated is proper.

2. If Group II is elected applicants are required to elect on of the following invention as required under 35 U.S.C. 121

II a- system for PCB (claims 28-40, and 46-48).

II b-System for connection (claims 41-45).

The inventions are distinct, each from the other because of the following reasons:

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Inventions IIa and IIb are related as subcombinations disclosed as usable together in a single combination. The subcombinations are distinct from each other if they are shown to be separately usable. In the instant case, invention II b has separate utility such as by the means of inserting one or more pin through a first through hole in the first circuit board and second through hole in the second circuit board to provide a second connection between the first and second boards, etc. See MPEP § 806.05(d).

Because these inventions are distinct for the reasons given above and have acquired a separate status in the art as shown by their different classification, and the search required for Group I is not required for either of Group II-IV, restriction for examination purposes as indicated is proper.

Because these inventions are distinct for the reasons given above and have acquired a separate status in the art because of their recognized divergent subject matter, restriction for examination purposes as indicated is proper.

3. Applicant is advised that the reply to this requirement to be complete must include an election of the invention to be examined even though the requirement be traversed (37 CFR 1.143). Applicant is reminded that upon the cancellation of claims to a non-elected invention, the inventorship must be amended in compliance with 37 CFR 1.48(b) if one or more of the currently named inventors is no longer an inventor of at least one claim remaining in the application. Any amendment of inventorship must be accompanied by a request under 37 CFR 1.48(b) and by the fee required under 37 CFR 1.17(i).

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4. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Minh Trinh whose telephone number is (571) 272-4569. The examiner can normally be reached on Monday -Thursday 8:00 am to 4:30 pm.

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If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Peter Vo can be reached on (571) 272-4690. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

mt 11/21/05

MINHTRINH
PRIMARY EXAMINER